

Docket No.: 50090-449

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of
Takashi TERAUCHI, et al.

Serial No.: 09/976,341

Group Art Unit: 2815

Filed: October 15, 2001

Examiner: B. Baumeister

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, AND
SEMICONDUCTOR DEVICE HAVING MEMORY CELL**

#6/Elect
5/7/02
VS

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, DC 20231

RECEIVED
APR 15 2002
TC-2800 MAIL ROOM


Sir:

Noting the Office Action of March 15, 2002 wherein restriction has been required, Applicant(s) hereby elect Group I (claims 1-14) for prosecution in the above-identified application.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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